

FX1 Series Datasheet

FPGA interface module

Host bridge: PCIe or board-to-board integration depending on project requirements.

I/O options: GPIO, LVDS, SERDES, timing, trigger, and custom protocol lanes.

Core blocks: interface logic, protocol adaptation, routing, timing-aware validation.

Primary use: bridging custom devices into controlled host systems.

Support scope: FPGA logic, protocol bridge, host interface, validation utilities.

Note: Throughput and timing are project-specific and interface dependent.