

PX4-DMA Series Datasheet

PCIe DMA acquisition card

Host interface: PCIe Gen3 x4 endpoint reference architecture.

Reference throughput: up to 3.2 GB/s sustained DMA in validation workload.

Core blocks: FPGA buffering, packetization, flow control, host DMA path.

Primary use: high-speed acquisition and streaming into host memory.

Support scope: board configuration, DMA path tuning, driver adaptation.

Validation note: 8 hour continuous reference stream with application-level integrity checks.